

**Search Notes**

Application No.

09/955,961

Examiner

Christopher E. Lee

Applicant(s)

ANDRE, GREGORY S.

Art Unit

2112

**SEARCHED**

Class	Subclass	Date	Examiner
710	243 107 6	3/11/2004	CEL
711	122 141	3/11/2004	CEL
348	164	3/11/2004	CEL
Search	Updated	11/18/2004	CEL
710	302	3/9/2005	CEL
700	97	3/9/2005	CEL
370	414	3/9/2005	CEL
712	35	9/5/2005	CEL
370	389	9/5/2005	CEL
710	52	12/10/2005	CEL
Above	UPDATED	4/25/2006	CEL

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
EAST Search (USPT, JPO, EPO, IBM_TDB) - Multi-tier Bus, Arbitration, Multiprocessing System; Inventor Search being processed	3/11/2004	CEL
Search Updated	11/18/2004	CEL
EAST Search - Arbitration for Network Switching	3/9/2005	CEL
EAST - Packet-Based Bus architecture	9/5/2005	CEL
EAST - buffer managing for packet communication	12/10/2005	CEL
Search Updated	4/25/2006	CEL